



501.36127CC3

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: KOUBUCHI et al  
Serial No.: 10/619,039  
Filed: July 14, 2003  
For: Semiconductor Integrated Circuit Device  
Group: 2814  
Examiner: L. Pham

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR 1.97 & 1.98**

Mail Stop: DD  
Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

April 22, 2004

Sir:

In the matter of the above-identified application, applicants are submitting herewith a copy of the document listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted before the mailing date of either a final action or a notice of allowance and is accompanied by the fee of \$180.00 set forth in 37 CFR 1.17(p).

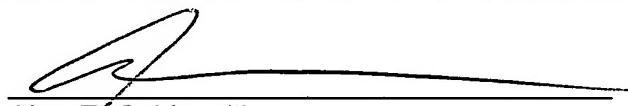
The documents listed on the attached form equivalent to Form PTO-1449 is in the English language.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing  
of this paper, including extension of time fees, to the deposit account of  
Antonelli, Terry, Stout & Kraus, LLP, Deposit Account No. 01-2135 (Case:  
501.36127CC3), and please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP



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Attachments

**FORM PTO-1449** U.S. Department of Commerce  
(Rev. 4/92) Patent and Trademark Office

## **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(Use several sheets if necessary)

ATTY. DOCKET NO.  
501.36127CC3

SERIAL NO.

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**APPLICANT**

FILING DATE  
July 14, 2003

GROUP  
2814

## **U.S. PATENT DOCUMENTS**

## **FOREIGN PATENT DOCUMENTS**

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**


**EXAMINER**

**DATE CONSIDERED**

**EXAMINER:** Initial if citation is considered, draw line through citation if not in conformance and not considered.  
Include copy of this form with next communication to applicant.